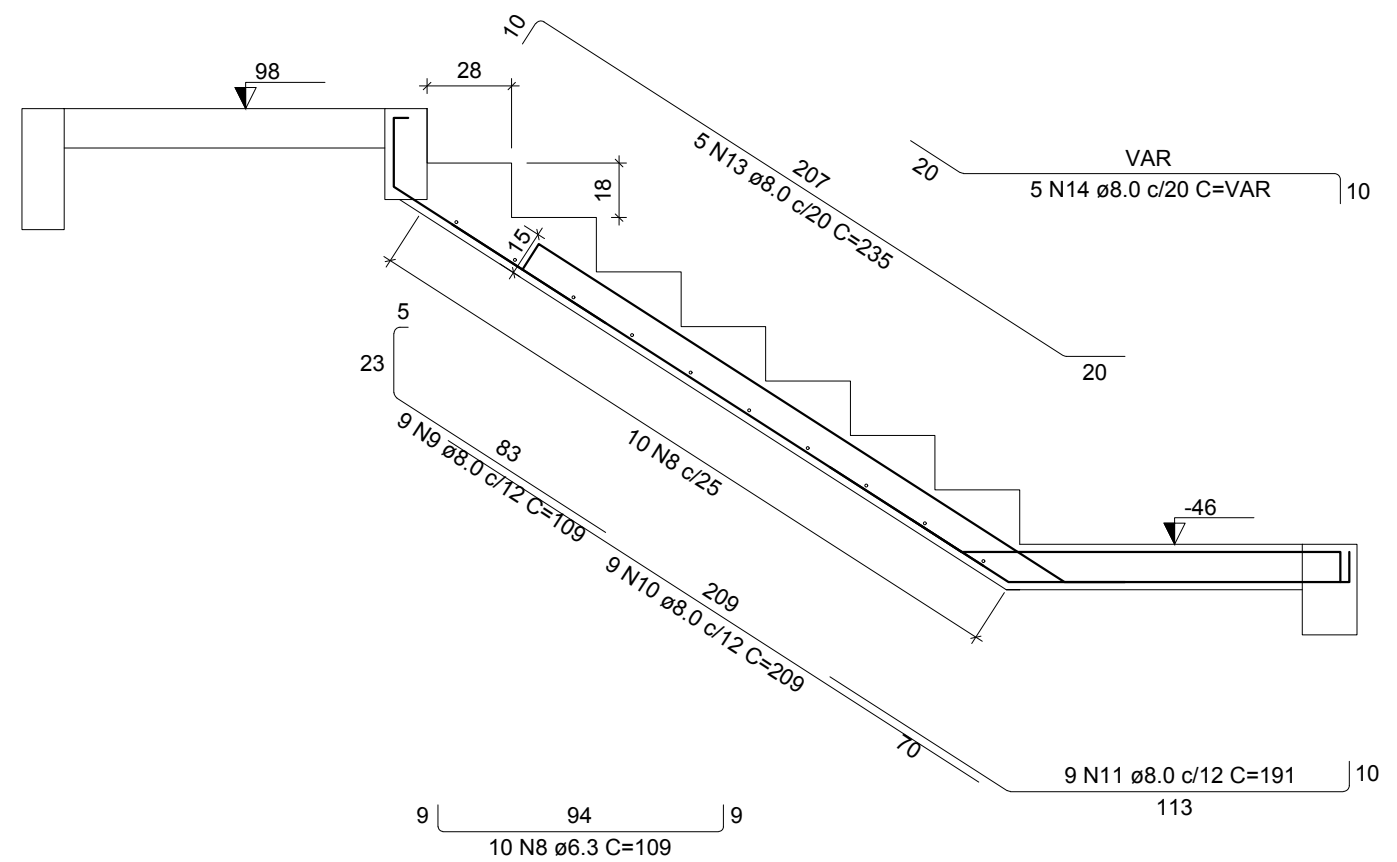


Corte A-A (LE1)

ESC 1:25



Corte B-B (LE3)

ESC 1:25